Really fast syndrome-based hashing

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Joint work with Daniel J. Bernstein, Tanja Lange, Christiane Peters

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Introduction - Hash functions



- Function $h: \{0,1\}^* \rightarrow \{0,1\}^n$
- Preimage resistance: Given h(M), infeasible to find M
- ▶ Second preimage resistance: Given M, infeasible to find $M' \neq M$ with h(M) = h(M')
- \blacktriangleright Collision resistance: Infeasible to find M,M', with $M\neq M'$ and h(M)=h(M')

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- \blacktriangleright Collision resistance: Infeasible to find M,M', with $M\neq M'$ and h(M)=h(M')
- "Trivial" property: Hash functions irreversibly compress arbitrarily long strings
- Arbitrarily long usully means: Some sort of iterative process

Merkle-Damgård iteration



▶ Use fixed-input-length compression function $F: \{0,1\}^\ell \to \{0,1\}^k$ with $\ell > k$



• Apply output filter $\{0,1\}^k \to \{0,1\}^n$

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- Apply output filter $\{0,1\}^k \to \{0,1\}^n$
- \blacktriangleright In the following: Zoom into F

A simple construction for F



- ► Consider input of length $\ell = w \cdot b$, hence, $m = (m_1, m_2, ..., m_w)$, each m_i with b bits
- ► Take an r × w2^b binary (pseudo-)random matrix, decomposed into w blocks with 2^b columns each: C = (c₁, c₂,..., c_w)





How about collisions?





- \blacktriangleright Resistance obviously depends on $b,\,w,$ and r
- ► Larger *r* makes it harder to find collisions (but reduces compression factor)
- ► Smaller *w* or *b* makes it harder to find collisions (but reduces compression factor)

Specifying the parameters



- ► Long history of compression functions with similar constructions
- ▶ ... also long history of breaks (see paper)
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FSB-256

- ► FSB is a SHA-3 round-1 candidate by Augot, Finiasz, Gaborit, Manuel, Sendrier
- FSB-256 is designed to provide 2^{128} bits of security against collisions
- Parameters: b = 14, w = 128, r = 1024

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RFSB-509

- ▶ RFSB is the family of compression functions described in this paper
- RFSB-509 is designed to provide 2¹²⁸ bits of security against collisions
- Parameters: b = 8, w = 112, r = 509



- FSB is unbroken, but did not make it to round-2 of the SHA-3 competition
- ▶ Reason: It is too slow, 95.53 cycles/byte on an Intel Core 2 Quad Q9550
- ► Comparison: SHA-256 takes just 15.26 cycles/byte on the same machine



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- ▶ For FSB use p = 1061 and construct $c_i[0], c_i[1], \ldots, c_i[16383]$ as

$$c_i[0], \quad c_i[0]X, \quad c_i[0]X^2, \dots, \quad c_i[0]X^{1023},$$

 $c_i[1024], \quad c_i[1024]X, \quad c_i[1024]X^2, \dots, \quad c_i[1024]X^{1023},$

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 $c_i[15360], c_i[15360]X, c_i[15360]X^2, \dots, c_i[15360]X^{1023}$

• Note that rotation distances (exponents of X) depend on input



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- Hand-optimized assembly implementation (for AMD64)
- Implementation-aware design





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- \blacktriangleright Let's look at ways to generate collisions, i.e. 2w columns, 2 per block, that add up to zero



- ► Idea: Start with 2^t lists containing (sums of) columns, proceed in various levels:
- \blacktriangleright In each level obtain 2^{i-1} lists from 2^i lists through merging
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- Compression functions of full FSB versions are similarly over-dimensioned



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- RFSB is designed to protect against 2-regular ISD
- ▶ We presented improved 2-regular ISD at IWCC 2011
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- Finding 2-regular low-weight codewords is not as well studied as finding general low-weight codewords
- ► We encourage the community to try to improve our complexity bounds

Detailed description of the implementation

- Extra speed: incremental hashing
- Extra speed: fast batch verification of hashes

Some more history of designs and breaks

- Extra security: Elimination of variable-index table lookups (at the expense of speed)
- Detailed attack analysis with some new generalizations

Full specification of RFSB and RFSB-509 (including matrix

More in the paper

generation)



Conclusion



- ▶ RFSB-509 is faster than 7 out of 14 SHA-3 round-2 candidates
- ▶ RFSB-509 is faster than 3 out of 5 SHA-3 finalists
- Software is in the public domain, submitted to eBASH for public benchmarking

Paper online: http://eprint.iacr.org/2011/074/