

Engineering Cryptographic Software

Scalar Multiplication

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New Directions in Cryptography

Invited Paper

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Abstract—Two kinds of contemporary developments in cryptography are examined. Widening applications of teleprocessing have given rise to a need for new types of cryptographic systems, which minimize the need for secure key distribution channels and supply the equivalent of a written signature. This paper suggests ways to solve these currently open problems. It also discusses how the theories of communication and computation are beginning to provide the tools to solve cryptographic problems of long standing.

I. INTRODUCTION

WE STAND TODAY on the brink of a revolution in cryptography. The development of cheap digital

The best known cryptographic problem is that of privacy: preventing the unauthorized extraction of information from communications over an insecure channel. In order to use cryptography to insure privacy, however, it is currently necessary for the communicating parties to share a key which is known to no one else. This is done by sending the key in advance over some secure channel such as private courier or registered mail. A private conversation between two people with no prior acquaintance is a common occurrence in business, however, and it is unrealistic to expect initial business contacts to be postponed long enough for keys to be transmitted by some physical means. The cost and delay imposed by this key distribution problem is a major barrier to the transfer of business



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- ▶ For all $a, b \in S$: $a \circ b \in S$ (closed under \circ)
- ▶ For all $a, b, c \in S$: $(a \circ b) \circ c = a \circ (b \circ c)$ (associativity)
- ▶ There exists $u \in S$ such that for any $a \in S$: $a \circ u = u \circ a = a$ (identity element)
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- ▶ We call $g \in S$ a *generator* of the group if all elements of S can be written as multiples (additive group) or powers (multiplicative group) of g .
- ▶ A group generated by a single element is called *cyclic*

Examples



- ▶ The integers with addition $(\mathbb{Z}, +)$ are a (commutative) group
 - ▶ Closed, associative ✓
 - ▶ Identity element 0
 - ▶ Inverse of a is $-a$
 - ▶ The group is cyclic with generator 1

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 - ▶ More generally we lack inverses, e.g., $\frac{1}{2} \notin \mathbb{Z}$
- ▶ The rationals without zero with multiplication $(\mathbb{Q} \setminus \{0\}, \cdot)$ are a (commutative) group
 - ▶ Closed, associative ✓
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 - ▶ Inverse of a is $\frac{1}{a}$



- ▶ For an integer $q > 1$, the set $\{0, \dots, q - 1\}$ together with addition modulo q is a (commutative) group
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- ▶ For a prime q , the set $\{1, \dots, q - 1\}$ together with multiplication modulo q is a (commutative) group
 - ▶ Closed, associative ✓
 - ▶ Identity element is 1
 - ▶ More about inverses later



Definition

Let G be a finite, Abelian, cyclic group of order ℓ with generator g . Let a be an element of G . The (*computational*) *discrete-logarithm problem (DLP)* is

- ▶ to find an integer k such that $g^k = a$ (for a multiplicatively written group)
- ▶ to find an integer k such that $kg = a$ (for an additively written group)

- ▶ g^k means $\underbrace{g \cdot g \cdot g \cdots \cdots g}_{k \text{ times}}$
- ▶ kg means $\underbrace{g + g + g + \cdots + g}_{k \text{ times}}$



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- ▶ g^k means $\underbrace{g \cdot g \cdot g \cdots \cdots g}_{k \text{ times}}$
- ▶ kg means $\underbrace{g + g + g + \cdots + g}_{k \text{ times}}$
- ▶ In many groups the DLP is easy to solve (e.g., $\{0, \dots, q-1\}$ with addition modulo q)
- ▶ In some groups the DLP is believed to be hard (e.g., $\{1, \dots, q-1\}$ with multiplication modulo q), for certain primes q



For the remainder of today's lecture

- ▶ consider an finite, cyclic group G , written additively,
- ▶ the generator of G is called P ,
- ▶ the group order of G is ℓ ,
- ▶ other elements are denoted by capital letters (e.g., P, R), and
- ▶ we assume that the DLP is hard in G .

Diffie-Hellman (DH) key exchange



Alice

Bob

Choose $a \leftarrow \{0, \dots, \ell - 1\}$

Choose $b \leftarrow \{0, \dots, \ell - 1\}$

$A \leftarrow aP$

$B \leftarrow bP$

A



B



$K \leftarrow aB = a(bP) = (ab)P$

$K \leftarrow bA = b(aP) = (ba)P$

Somes notes about DH



- ▶ Clearly insecure of DLP is not hard



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- ▶ Also secure only against passive adversaries
- ▶ Active “man-in-the-middle” (MitM) attack:
 - ▶ Eve chooses her own keypair (e, E)
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- ▶ DH is an **unauthenticated** key exchange
- ▶ Consider DH a **building block** for protocols

How about authentication?



- ▶ Can build **authenticated** key exchange just from DH (plus symmetric primitives)
- ▶ Examples:
 - ▶ X3DH used by Signal (<https://signal.org/docs/specifications/x3dh/>)
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 - ▶ $\text{accept/reject} \leftarrow \text{Verify}(\text{msg}, \text{sig}, vk)$ (deterministic)



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For (sk, vk) generated by KeyGen and sig generated by $\text{Sign}(\text{msg}, \text{sk})$, we want $\text{Verify}(\text{msg}, \text{sig}, \text{vk})$ to return accept (with overwhelming probability).



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- ▶ This is called **existential unforgeability under chosen-message attacks (EUF-CMA)**



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$\text{Verify}(\text{msg}, \text{sig} = (R, S), \text{vk} = A)$

- ▶ Compute $\bar{R} \leftarrow sP + eA$
- ▶ Return accept if and only if $H(\bar{R}, \text{msg}) = e$

Scalar multiplication



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 - ▶ The scalars in Schnorr signature verification are public
- ▶ In the following: Distinguish these cases

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- ▶ Conclusion: we need algorithms that run in polynomial time (in the size of the scalar)

Rewriting the scalar



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- ▶ Cost: 6 doublings, 3 additions
- ▶ General algorithm: "Double and add"

```
R ← P
for i ← n - 2 downto 0 do
    R ← 2R
    if (k)2[i] = 1 then
        R ← R + P
    end if
end for
return R
```



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- ▶ On average: $\approx n/2$ additions
- ▶ P does not need to be known in advance, no precomputation depending on P



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- ▶ Double-and-add takes $n - 1$ doublings
- ▶ Let m be the number of 1 bits in the exponent
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- ▶ On average: $\approx n/2$ additions
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- ▶ Handles single-scalar multiplication
- ▶ Running time clearly depends on the scalar: insecure for secret scalars!

Double-scalar double-and-add

- ▶ Let's modify the algorithm to compute $k_1 P_1 + k_2 P_2$



Double-scalar double-and-add



- ▶ Let's modify the algorithm to compute $k_1 P_1 + k_2 P_2$
- ▶ Obvious solution:
 - ▶ Compute $k_1 P_1$ ($n_1 - 1$ doublings, $m_1 - 1$ additions)
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 - ▶ Add the results (1 addition)
- ▶ We can do better (\mathcal{O} denotes the neutral element):

```
R ←  $\mathcal{O}$ 
for  $i \leftarrow \max(n_1, n_2) - 1$  downto 0 do
     $R \leftarrow 2R$ 
    if  $(k_1)_2[i] = 1$  then
         $R \leftarrow R + P_1$ 
    end if
    if  $(k_2)_2[i] = 1$  then
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- ▶ $\max(n_1, n_2)$ doublings, $m_1 + m_2$ additions

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- ▶ Whenever k_1 and k_2 have a 1 bit at the same position, we first add P_1 and then P_2 (on average for 1/4 of the bits)

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- ▶ Whenever k_1 and k_2 have a 1 bit at the same position, we first add P_1 and then P_2 (on average for 1/4 of the bits)
- ▶ Let's just precompute $T = P_1 + P_2$
- ▶ Modified algorithm (Shamir's trick, special case of Strauss' algorithm):

```
 $R \leftarrow \mathcal{O}$ 
for  $i \leftarrow \max(n_1, n_2) - 1$  downto 0 do
     $R \leftarrow 2R$ 
    if  $(k_1)_2[i] = 1$  AND  $(k_2)_2[i] = 1$  then
         $R \leftarrow R + T$ 
    else if  $(k_1)_2[i] = 1$  then
         $R \leftarrow R + P_1$ 
    else if  $(k_2)_2[i] = 1$  then
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    end if
end for
return  $R$ 
```

Even more (offline) precomputation

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- ▶ This needs only about 16KB of storage for $n = 256$ and 64-byte group elements

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- ▶ Eliminated all doublings in fixed-basepoint scalar multiplication!

Double-and-add always



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- ▶ For secret scalars (most common case!) we need something else



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- ▶ Still not constant time, more later...



- ▶ We have a table $T = (\mathcal{O}, P)$
- ▶ Notation $T[0] = \mathcal{O}, T[1] = P$
- ▶ Scalar multiplication is

```
 $R \leftarrow P$ 
for  $i \leftarrow n - 2$  downto 0 do
     $R \leftarrow 2R$ 
     $R \leftarrow R + T[(k)_2[i]]$ 
end for
```

Changing the scalar radix



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```
 $R \leftarrow T[(k)_3[n - 1]]$ 
for  $i \leftarrow n - 2$  downto 0 do
     $R \leftarrow 3R$ 
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end for
```

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- ▶ Advantage: The scalar is shorter, fewer additions
- ▶ Disadvantage: 3 is just not nice (needs triplings)

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- ▶ Advantage: The scalar is shorter, fewer additions
- ▶ Disadvantage: 3 is just not nice (needs triplings)
- ▶ How about some nice numbers, like 4, 8, 16?



- ▶ Fix a window width w
- ▶ Precompute $T = (\mathcal{O}, P, 2P, \dots, (2^w - 1)P)$



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```
 $R \leftarrow T[(k)_{2^w}[m - 1]]$ 
for  $i \leftarrow m - 2$  downto 0 do
  for  $j \leftarrow 1$  to  $w$  do
     $R \leftarrow 2R$ 
  end for
   $R \leftarrow R + T[(k)_{2^w}[i]]$ 
end for
```



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- ▶ For ≈ 256 -bit scalars choose $w = 4$ or $w = 5$

Is fixed-window constant time?



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- ▶ See [assignment2-ecdh25519](#)

More offline precomputation



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- ▶ We can combine that with fixed-window scalar multiplication
- ▶ Precompute $T_i = (\mathcal{O}, P, 2P, 3P, \dots, (2^w - 1)P) \cdot 2^i$ for $i = 0, w, 2w, 3w, \lceil n/w \rceil - 1$



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- ▶ Perform scalar multiplication as

```
R ← T0[(k)2w[0]]  
for i ← 1 to  $\lceil n/w \rceil - 1$  do  
    R ← R + Tiw[(k)2w[i]]  
end for
```



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- ▶ Perform scalar multiplication as

```
 $R \leftarrow T_0[(k)_{2^w}[0]]$ 
for  $i \leftarrow 1$  to  $\lceil n/w \rceil - 1$  do
     $R \leftarrow R + T_{iw}[(k)_{2^w}[i]]$ 
end for
```

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- ▶ No doublings, only $\lceil n/w \rceil - 1$ additions
- ▶ Can use huge w , but:
 - ▶ at some point the precomputed tables don't fit into cache anymore.
 - ▶ constant-time loads get slow for large w



- ▶ Consider the scalar $22 = (1\ 01\ 10)_2$ and window size 2
 - ▶ Initialize R with P
 - ▶ Double, double, add P
 - ▶ Double, double, add $2P$



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- ▶ More efficient:
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 - ▶ Double, double, double, add $3P$
 - ▶ Double
- ▶ Problem with fixed window: it's fixed.
- ▶ Idea: "slide" the window over the scalar

Sliding window scalar multiplication



- ▶ Choose window size w
- ▶ Rewrite scalar k as $k = (k_0, \dots, k_m)$ with k_i in $\{0, 1, 3, 5, \dots, 2^w - 1\}$ with at most one non-zero entry in each window of length w



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- ▶ Do this by scanning k from right to left, expand window from each 1-bit



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- ▶ Do this by scanning k from right to left, expand window from each 1-bit
- ▶ Precompute $P, 3P, 5P, \dots, (2^w - 1)P$
- ▶ Perform scalar multiplication

```
R ← O
for i ← m to 0 do
    R ← 2R
    if  $k_i \neq 0$  then
        R ← R +  $k_i P$ 
    end if
end for
```



- ▶ We still do $n - 1$ doublings for an n -bit scalar
- ▶ Precomputation needs $2^{w-1} - 1$ additions
- ▶ Expected number of additions in the main loop: $n/(w + 1)$



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- ▶ For the same w only half the precomputation compared to fixed-window scalar multiplication
- ▶ For the same w fewer additions in the main loop
- ▶ But: It's not running in constant time!
- ▶ Still nice (in double-scalar version) for signature verification



- ▶ Consider computation $Q = \sum_1^n k_i P_i$
- ▶ We looked at $n = 2$ before, how about $n = 128$?

De-Rooij algorithm

- ▶ Assume $k_1 > k_2 > \dots > k_n$.
- ▶ Use that $k_1 P_1 + k_2 P_2 = (k_1 - k_2)P_1 + k_2(P_1 + P_2)$
- ▶ Replace:
 - ▶ $(k_1 P_1)$ and $(k_2 P_2)$, with
 - ▶ $(k_1 - k_2)P_1$ and $k_2(P_1 + P_2)$
- ▶ Each step requires one scalar subtraction and one point addition
- ▶ Each step typically “eliminates” multiple scalar bits
- ▶ Can be very fast (but not constant-time)



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- ▶ Each step requires one scalar subtraction and one point addition
- ▶ Each step typically “eliminates” multiple scalar bits
- ▶ Can be very fast (but not constant-time)
- ▶ Requires fast access to the two largest scalars: put scalars into a heap
- ▶ Crucial for good performance: fast heap implementation



- ▶ Heap is a binary tree, each parent node is larger than the two child nodes
- ▶ Data structure is stored as a simple array, positions in the array determine positions in the tree
- ▶ Root is at position 0, left child node at position 1, right child node at position 2 etc.
- ▶ For node at position i , child nodes are at position $2 \cdot i + 1$ and $2 \cdot i + 2$, parent node is at position $\lfloor (i - 1)/2 \rfloor$



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- ▶ Typical heap root replacement (pop operation): start at the root, swap down for a variable amount of times
- ▶ Floyd's heap: swap down to the bottom, swap up for a variable amount of times, advantages:
 - ▶ Each swap-down step needs only one comparison (instead of two)
 - ▶ Swap-down loop is more friendly to branch predictors

How about fixed scalar



- ▶ So far we have considered:
 - ▶ **variable** point, **variable** scalar
 - ▶ **fixed** point, **variable** scalar

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- ▶ How about **variable** point, **fixed** scalar?
- ▶ Optimizing for the scalar means that the scalar has to be public
- ▶ Not what we have in DH or Schnorr



- ▶ So far we have considered:
 - ▶ **variable** point, **variable** scalar
 - ▶ **fixed** point, **variable** scalar
- ▶ How about **variable** point, **fixed** scalar?
- ▶ Optimizing for the scalar means that the scalar has to be public
- ▶ Not what we have in DH or Schnorr
- ▶ Some applications:
 - ▶ Inversion in finite fields (later in this course)
 - ▶ Elliptic-curve factorization method (not in this lecture)



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- ▶ For fixed scalar we can spend a lot of time to find a good addition chain at compile time
- ▶ Computing good addition chains? See <https://github.com/mmcloughlin/addchain>